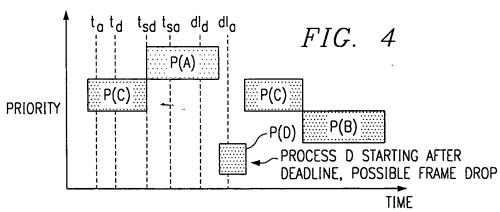


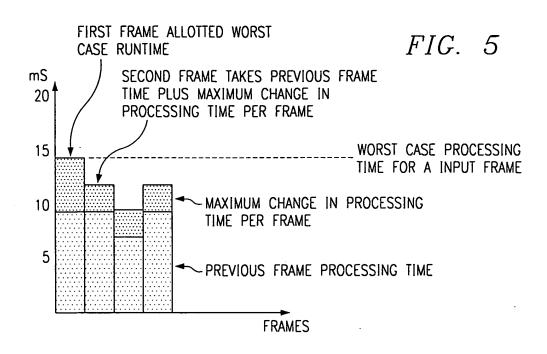
 $t_{SG}$  = LAST POSSIBLE TIME FOR PROCESS A TO START AND STILL MAKES ITS DEADLINE

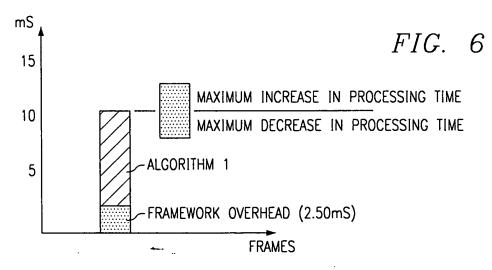
 $t_{sd}$  = LAST POSSIBLE TIME FOR PROCESS D TO START AND STILL MAKE ITS DEADLINE

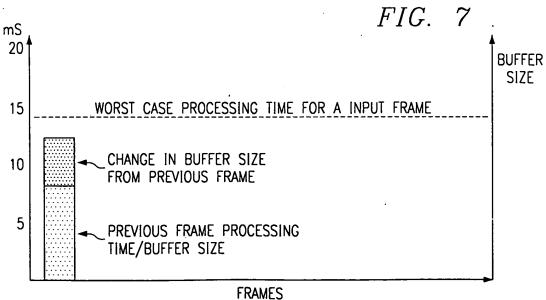


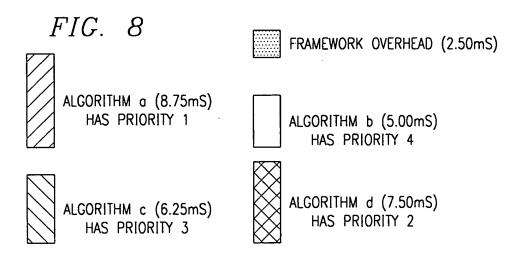
 $\mathfrak{t}_{SO}$  = LAST POSSIBLE TIME FOR PROCESS A TO START AND STILL MAKES ITS DEADLINE

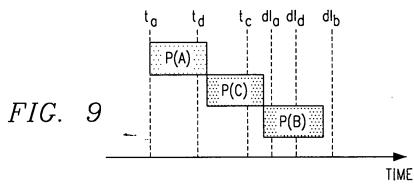
 $t_{sd}$  = LAST POSSIBLE TIME FOR PROCESS D TO START AND STILL MAKE ITS DEADLINE







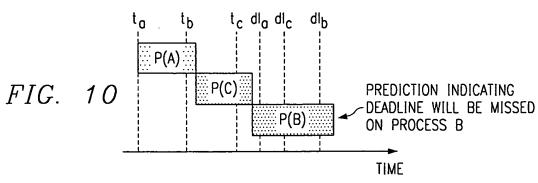




t; = TIME STAMP ARRIVAL OF EACH DATA FRAME FOR THE RESPECTIVE PROCESS

di; = DEADLINE FOR FINISHING PROCESSING OF EACH RECEIVED DATA FRAME

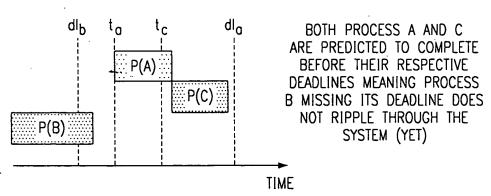
P() = PREDICTION OF PROCESSING TIME FOR EACH RECEIVED DATA FRAME



t; = TIME STAMP ARRIVAL OF EACH DATA FRAME FOR THE RESPECTIVE PROCESS

di; = DEADLINE FOR FINISHING PROCESSING OF EACH RECEIVED DATA FRAME

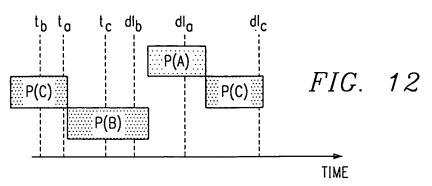
P() = PREDICTION OF PROCESSING TIME FOR EACH RECEIVED DATA FRAME



t; = TIME STAMP ARRIVAL OF EACH DATA FRAME FOR THE RESPECTIVE PROCESS

di; = DEADLINE FOR FINISHING PROCESSING OF EACH RECEIVED DATA FRAME

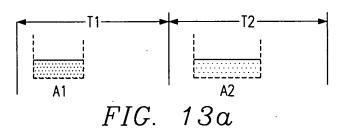
P() = PREDICTION OF PROCESSING TIME FOR EACH RECEIVED DATA FRAME FIG. 11

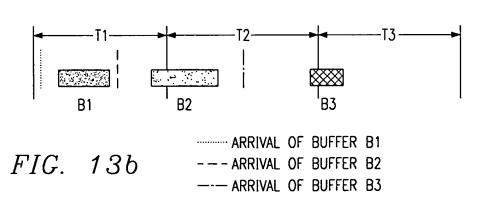


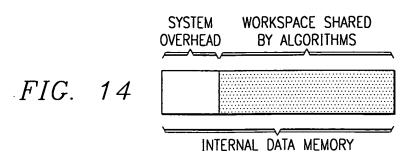
t; = TIME STAMP ARRIVAL OF EACH DATA FRAME FOR THE RESPECTIVE PROCESS

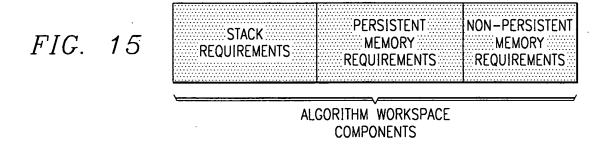
dI; = DEADLINE FOR FINISHING PROCESSING OF EACH RECEIVED DATA FRAME

P() = PREDICTION OF PROCESSING TIME FOR EACH RECEIVED DATA FRAME









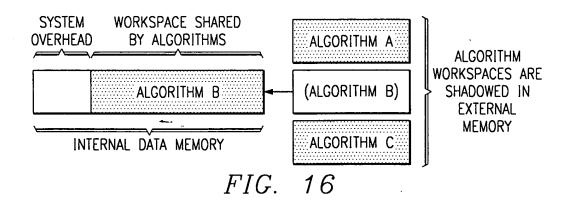


FIG. 17

STACK
PERSISTENT
MEMORY
MEMORY
REQUIREMENTS
REQUIREMENTS

ALGORITHM WORKSPACE COMPONENTS TO TRANSFER ON CONTEXT SWITCH

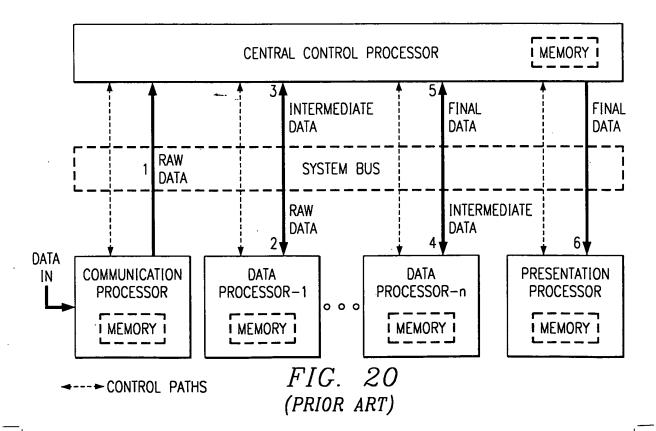
	STACK REQUIREMENTS	PERSISTENT MEMORY REQUIREMENTS	READ ONLY MEMORY REQUIREMENTS	MEMORY
--	-----------------------	--------------------------------------	-------------------------------	--------

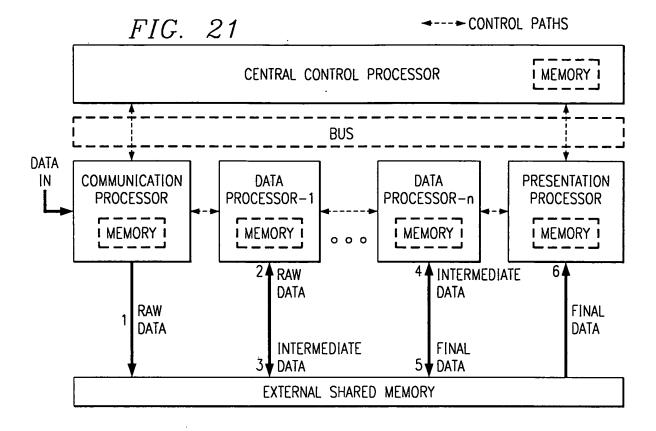
ALGORITHM WORKSPACE COMPONENTS TO TRANSFER IN PRIOR TO ALGORITHM EXECUTION IF ALGORITHM REQUIRES CONSTANT TABLES (CONTEXT SWITCH IN ONLY)

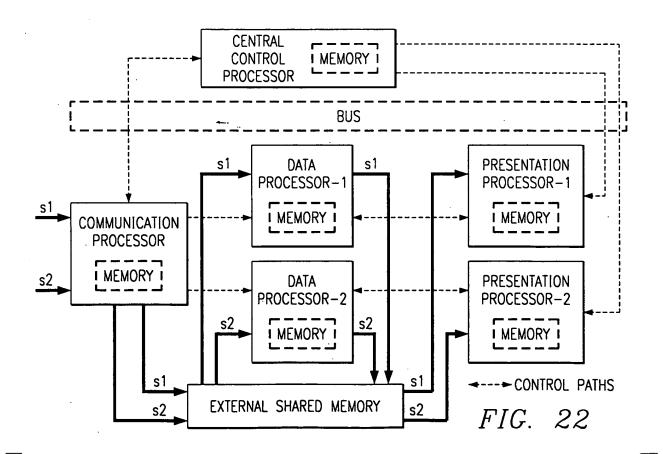
		DEDCICTENT	
,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	DEDCICTEMT	F LN3 3 L N	MAN DEDCICTENT
CTACK			
STACK		HILL KEAD UNLTHER	VEV.00V
		NOAD OIVET	
to REQUIREMENTS			
NEQUINEMENTS	REQUIREMENTS		·····REQUIREMENTS·····
	WWW. LEGOTHERING THAT	MODEL HOUSE NEW TO ME	NEQUINEMENTO
		∰ VEGOTIVEMENTO ∰	
		***************************************	

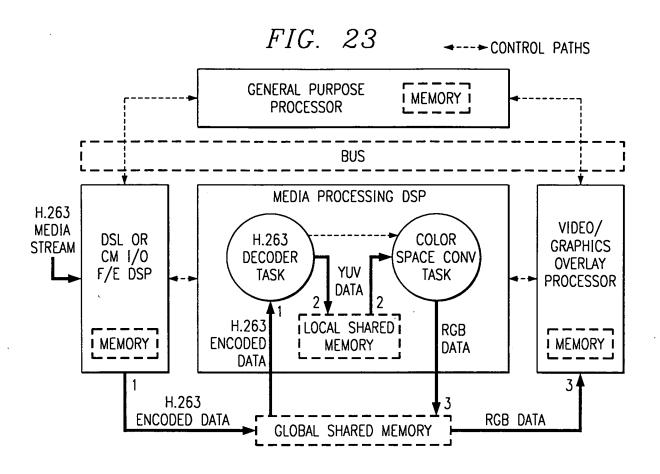
READ ONLY PERSISTENT MEMORY DOES NOT NEED TO BE TRANSFERRED OUT ON CONTEXT SWITCH. THEREFORE ALGORITHM PAGE CHANGE-OUT IS MORE EFFICIENT.

FIG. 19









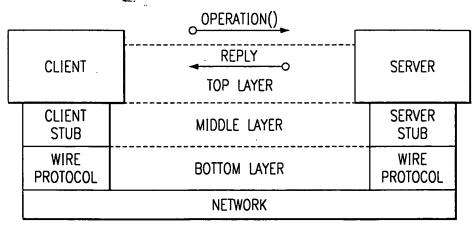
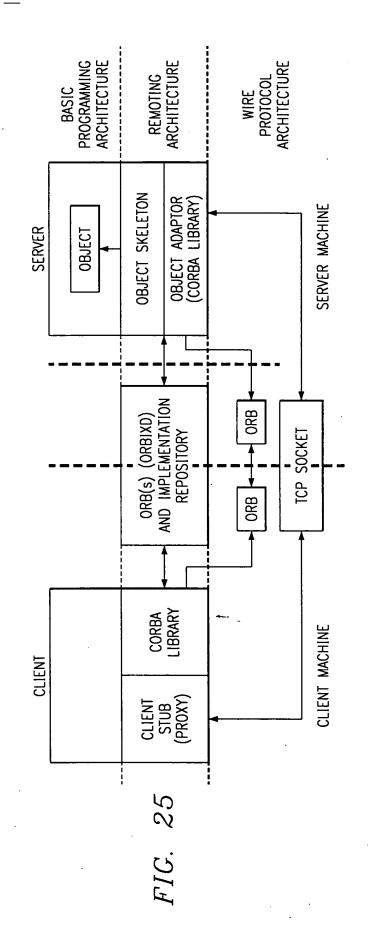
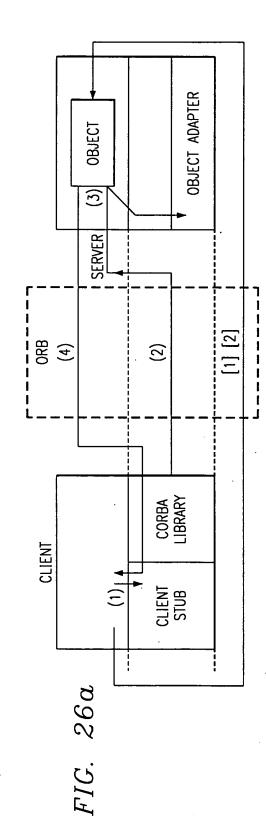
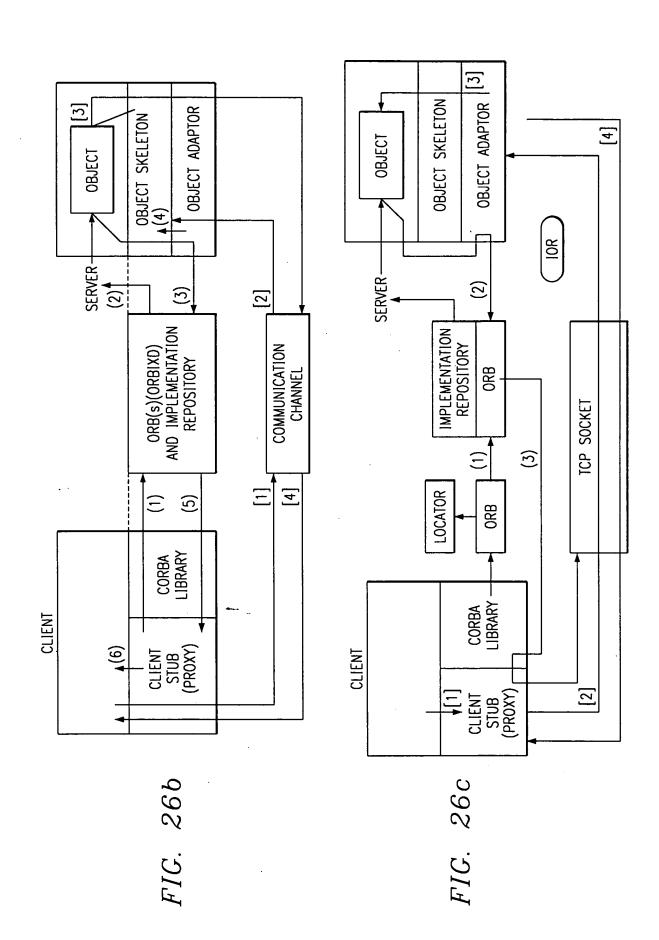


FIG. 24





, q.• - b



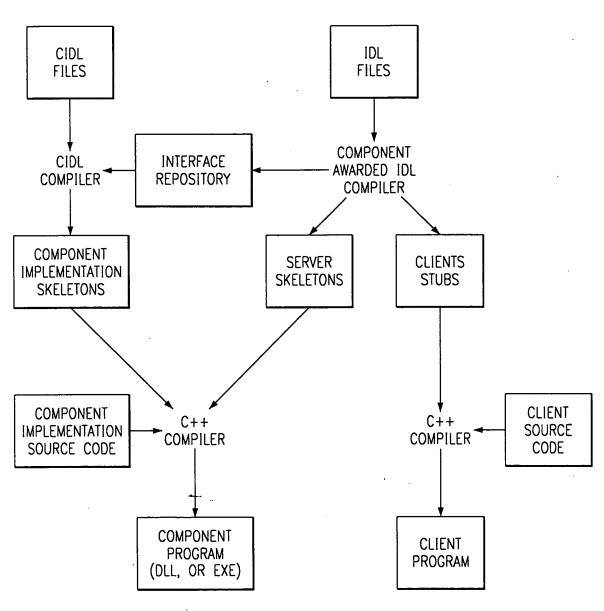


FIG. 27